200mA / 50V Digital transistor (with built-in resistor) DTC123TKA

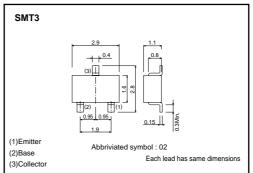
Applications

Inverter, Interface, Driver

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- Only the on/ off conditions need to be set for operation, making the device design easy.
- 4) Higher mounting densities can be achieved.

•External dimensions (Unit : mm)



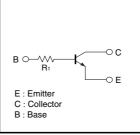
Structure

NPN epitaxial planar silicon transistor

Packaging specifications

	Package	SMT3
	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTC123TKA		0

Equivalent circuit



R1=2.2kΩ

Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	50	V	
Collector-emitter voltage	VCEO	50	V	
Emitter-base voltage	Vebo	5	V	
Collector current	lc	100	mA	
Collector Power dissipation	Pc	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

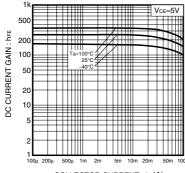
Transistors

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВИсво	50	-	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	50	-	_	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	5	-	_	V	Iε=50μA
Collector cutoff current	Ісво	-	-	0.5	μA	Vcb=50V
Emitter cutoff current	Іево	-	-	0.5	μA	VEB=4V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.3	V	Ic/IB=5mA/0.25mA
DC current transfer ratio	hfe	100	250	600	_	Ic=1mA , Vce=5V
Input resistance	R1	1.54	2.2	2.86	kΩ	_
Transition frequency	fт *	-	250	_	MHz	Vсв=10V , IE= –5mA , f=100MHz

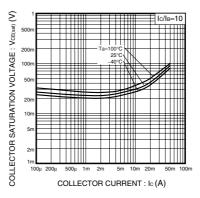
* Characteristics of built-in transistor.

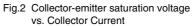
•Electrical characteristics curves



COLLECTOR CURRENT : Ic (A)

Fig.1 DC Current gain vs. Collector Current





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